

FIG. 1 (PRIOR ART)

Number of BIP violations	Bit 1	Bit 2	Bit 3	Bit 4
0	1	0	0	1
1	0	0	0	1
2	0	0	1	0
3	0	0	1	1
4	0	1	0	0
5	0	1	0	1
6	0	1	1	0
7	0	1	1	1
8	1	0	0	0
Incoming AIS	1	1	1	0

FIG. 2 (PRIOR ART)

Number of BIP violations	Bit 1	Bit 2	Bit 3	Bit 4	IEC Code Interpretation
0	1	0	0	1	0 BIP Violation
1	0	0	0	1	1 BIP Violation
2	0	0	1	0	2 BIP Violation
3	0	0	1	1	3 BIP Violation
4	0	1	0	0	4 BIP Violation
5	0	1	0	1	5 BIP Violation
6	0	1	1	0	6 BIP Violation
7	0	1	1	1	7 BIP Violation
8	1	0	0	0	8 BIP Violation
Incoming AIS	1	1	1	0	0 BIP Violation, Incoming AIS
Internal AIS	1	0	0	1	Unavailable, Internal AIS

FIG. 3

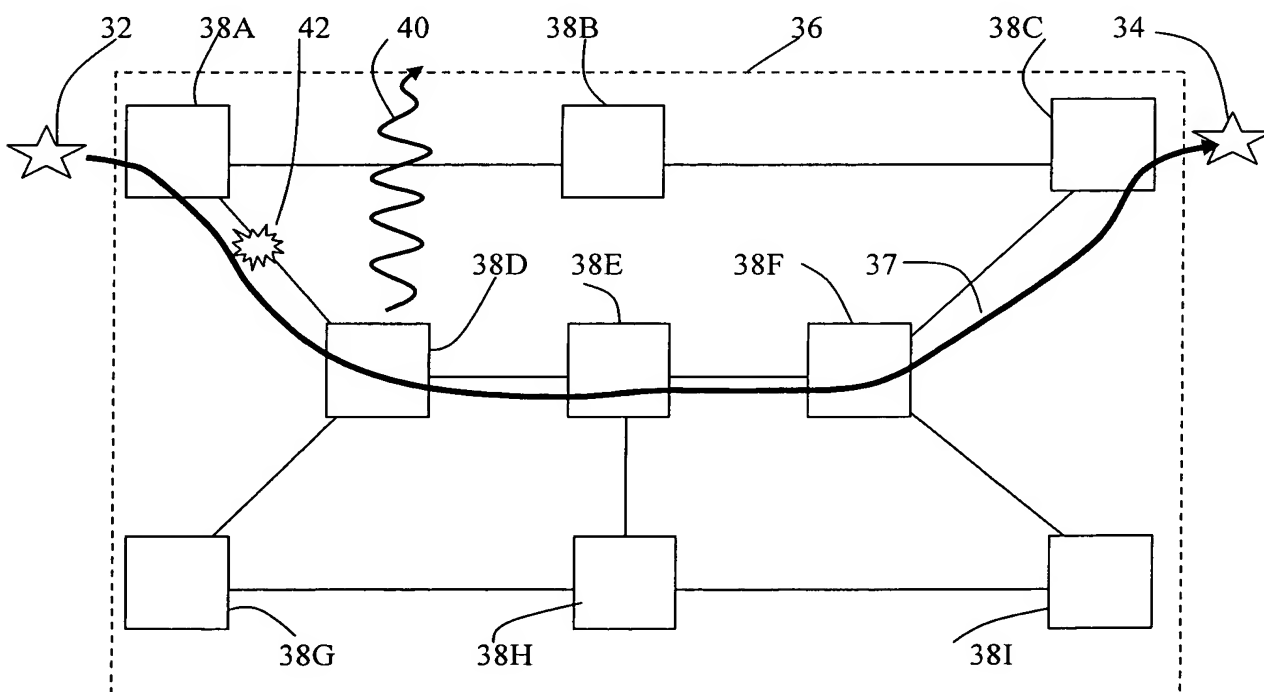


FIG. 4